



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
NOTICE OF APPEAL FROM THE PRIMARY EXAMINER  
TO THE BOARD OF PATENT APPEALS AND INTERFERENCES

In re application of:

GARY P. MORRISON ET AL.

Serial No.: 10/034,827 (TI-31373)

Filed: January 3, 2002

For: CHIP-SCALE PACKAGES STACKED ON FOLDED  
INTERCONNECTOR FOR VERTICAL ASSEMBLY ON SUBSTRATES

Group Art Unit: 2827

Examiner James M. Mitchell

Customer No. 23494

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CERTIFICATE OF MAILING OR TRANSMISSION UNDER 37 CFR 1.8

I hereby certify that the attached document is being deposited with the United States Postal Service with sufficient postage for First Class Mail in an envelope addressed to Director of the United States Patent and Trademark Office, P.O. Box 1450,, Alexandria, VA 22313-1450 or is being facsimile transmitted on the date indicated below:

8-20-04

Sir:

Applicant hereby appeals to the Board of Appeals from the decision dated June 16, 2004 of the Primary Examiner finally rejecting claims 2, 4 to 10, 12, 15, 17, 18, 20 and 23, all of the appealed rejected claims.

The item(s) checked below are appropriate:

1. ☐ An extension of time to respond to the final rejection  
☐ was granted on  
☐ is requested for \_\_\_\_\_ month(s).
2. ☐ A timely response to the final rejection has been filed, as provided in 841 O.G. 1411.
3. ☒ Fee \$330.00:  
☐ Not required (*Fee paid in prior appeal*)  
☒ The Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment to Account No. 20-0668. A duplicate copy of this sheet is enclosed.

Signature [Rule 191(b)]

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